



PATENT
8040-1052

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of
KUBOTA et al.

Application No. 10/763,244

Filed January 26, 2004

MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE AND OXIDIZATION
METHOD OF SEMICONDUCTOR SUBSTRATE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed sheet.

A concise explanation of the relevance of these items is that these references were discovered during any searches they or their client had made, or that they were considered in the preparation of the application.

Respectfully submitted,

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March 31, 2004

(Use several sheets if necessary)

Group Art Unit:

[illegible]

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	2000-269499	9/29/2000	JAPAN				
	6-267938	9/22/1994	JAPAN				
	63-316440	12/23/1988	JAPAN				
	11-274288	10/8/1999	JAPAN				

[illegible]

DATE CONSIDERED

* Abstract provided for the Examiner's convenience